GRP ART UNIT: 2827

] Ex.: Thai, Luan C.

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Jong Hwan KIM In re Application:

10/616,823 Serial No:

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For:

METHOD FOR FORMING BIT LINES OF SEMICONDUCTOR DEVICE

Certification under 37 C.F.R. §1.8(a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with The United States Postal Service with sufficient postage as first class mail in an envelope addressed to The Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on August 2

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AUTHORIZATION TO ACT IN A REPRESENTATIVE CAPACITY

The practitioner W. William Park, Reg. No. 55,523 is authorized to conduct interviews and has the authority to bind the principal concerned. Furthermore, the practitioner is authorized to file correspondence in the above-identified application pursuant to 37 CFR 1.34.

This is not a Power of Attorney to the above-named practitioner.

Accordingly, the practitioner named above does not have the authority to sign a request to change the correspondence address, a request for an express abandonment, a disclaimer, a power of attorney, or other document requiring the signature of the applicant, assignee of the entire interest or an attorney of record. If appropriate a separate Power of Attorney to the above named practitioner should be executed and filed in the United States Patent and Trademark Office.

Dated: August 2, 2004

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